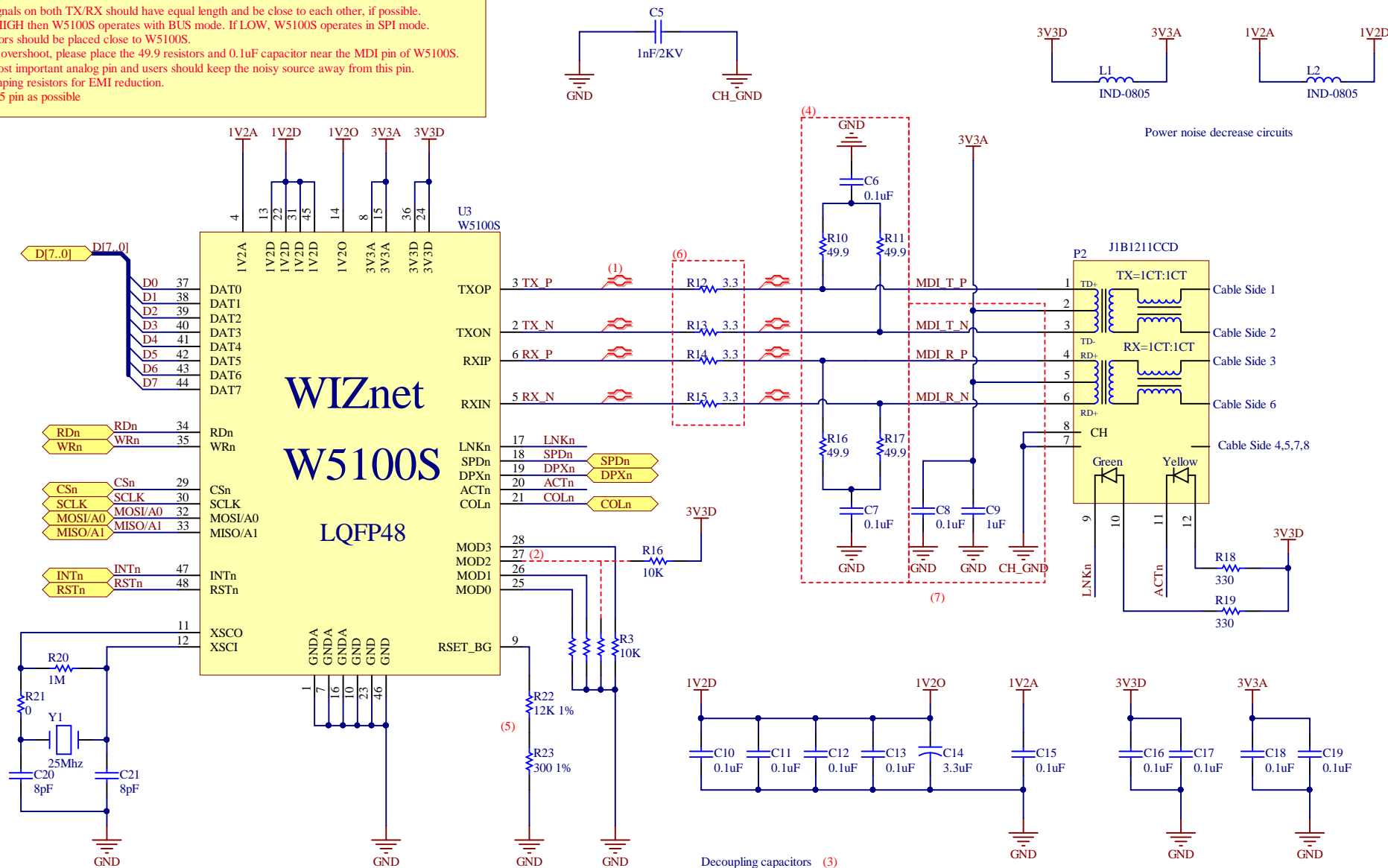



# !!NOTE!!

- (1) The differential signals on both TX/RX should have equal length and be close to each other, if possible.
- (2) When MOD[2] is HIGH then W5100S operates with BUS mode. If LOW, W5100S operates in SPI mode.
- (3) Decoupling capacitors should be placed close to W5100S.
- (4) To avoid the 100M overshoot, please place the 49.9 resistors and 0.1uF capacitor near the MDI pin of W5100S.
- (5) RSET\_BG is the most important analog pin and users should keep the noisy source away from this pin.
- (6) Option - Apply damping resistors for EMI reduction.
- (7) As close to RJ45 2,5 pin as possible



Title <b>W5100S Reference Schematic</b>			WIZnet Co., Ltd. 5F, Humax Village Hwangseaul-ro, Bundang-gu Seongnam-si, Gyeonggi-do South Korea		
Size: <b>A4</b>	Number:	Revision: <b>R1</b>			
Date: <b>2019-02-08</b>		Sheet <b>1</b> of <b>1</b>			
Team: <b>CS</b>		Drawn By: <b>*</b>			